

# Evaluation of LSI Inner Structure by Using Quasi-static Electrical Field Sensing Technology

Masaru Sanada<sup>1\*</sup> Seigo Ito<sup>2</sup>

(Received: April 30th, 2013)

<sup>1</sup> School of System Engineering, Kochi University of Technology  
185 Miyanokuchi, Tosayamada, Kami city, Kochi 782-8502, JAPAN

<sup>2</sup> Engineering Group, Konaka Electronics  
Softopia Japan Dream core 312, 52-16 Imajuku 6, Ogaki, Gifu 503-0807, JAPAN

\* E-mail: sanada.masaru@kochi-tech.ac.jp

**Abstract:** Quasi-static electrical field, being one of electrical fields which does not bring magnetic field, is distributed widely around devices. This electrical field is possible to measure with non-bias state. Additionally this field does not involve reflection and diffraction phenomenon. These features bring accurate evaluation result. By using measurement system combined QEF sensor\* unit with Laser microscope built-in OBIC function, an evaluation of LSI inner state was experimented. The evaluation outcomes were discrimination of substrate impurity type and also detection of void portion on metal line. These results indicated that this technology was suitable way to evaluate diffusion layer and substrate it. Concurrently, a couple of problems caused by measurement system were become clearly. The aim of this technology application is to obtain crystal defect distribution and mapping.

\*QEF sensor is named as NEPS (Nano Electrostatic field Probe Sensor).